

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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| APPL NUM 10015414 | FILING DATE 12/12/2001 | CLASS 711 | SUBCLASS 185.28 | GAU 2485 | EXAMINER PHAM |
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**CONTINUING DATA VERIFIED: no LP

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** FOREIGN APPLICATIONS VERIFIED: yes LP
TAIWAN 90129931 12/04/2001

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|---|---|----------------------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO |
| 35 USC 119 conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | JCLA7737 |
| Verified and Acknowledged Examiners's initials | LP | |
| TITLE : Method for programming and erasing non-volatile memory with nitride tunneling layer | | |

U.S. DEPT. OF COM. / PAT & TM-PTO-436L (Rev. 12-94)

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| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Assistant Examiner | Total Claims |
| | | | Print Claim for O.G. |
| ISSUE FEE | | Primary Examiner | DRAWING |
| Amount Due | Date Paid | | Sheets Drwg. Figs. Drwg. Print Fig. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | Application Examiner |
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